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generating a software test module to produce a test result by performing a test on instructions;
in the instructions, replacing a first instruction comprising a target address with a second non-identical instruction having an instruction address in the instructions, the second instruction to transfer control to the test module; and
storing the target address encrypted in a table, the test module to locate the target address in the table and to set an execution address to the target address if the test result indicates the instructions are to proceed.

2. The method of claim 1 further comprising
compacting the instructions to eliminate a hole created by replacing the first instruction with the second instruction.

3. The method of claim 1 further comprising:
corresponding the target address with the instruction address in the encrypted table.

4. The method of claim 1 further comprising:
profiling the instructions to identify the first instruction as an instruction to replace.

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5. A device comprising:
a processor;
a machine-readable storage medium coupled to the processor by way of a bus, the storage medium having stored thereon instructions which, when executed by the processor, cause the data processing device to
generate a software test module, the test module to produce a test result by performing a test on the instructions;
in the instructions, replace a first instruction comprising a target address with a second non-identical instruction having an instruction address in the instructions, the second instruction to transfer control to the test module; and

store the target address in an encrypted table, the test module to locate the target address in the table and to transfer control to the target address if the test result indicates the instructions are to proceed.

6. The device of claim 5 in which the instructions, when executed by the processor, further cause the device to:

compact the instructions to eliminate a hole created by replacing the first instruction with the second instruction.

7. The device of claim 5 in which the instructions, when executed by the processor, further cause the device to:

corresponding the target address with the instruction address in the encrypted table.

8. The device of claim 5 in which the instructions, when executed by the processor, further cause the device to:

profile the instructions to identify the first instruction as an instruction to replace.

9. An article comprising:

a machine-readable medium having stored thereon instructions which, when executed by a data processing device, cause the data processing device to:

generating a software test module to produce a test result by performing a test on the instructions;

in the instructions, replace a first instruction comprising a target address with a second non-identical instruction having an instruction address in the instructions, the second instruction to transfer control to the test module; and

store the target address in an encrypted table, the test module to locate the target address in the table and to transfer control to the target address if the test result indicates the instructions are to proceed.

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10. The article of claim 9 in which the instructions, when executed by a data processing device, further cause the data processing device to:
- compact the instructions to eliminate a hole created by replacing the first instruction with the second instruction.
11. The article of claim 9 in which the instructions, when executed by a data processing device, further cause the data processing device to:
- correspond the target address with the instruction address in the encrypted table.
12. The article of claim 9 in which the instructions, when executed by a data processing device, further cause the data processing device to:
- profile the instructions to identify the first instruction as an instruction to replace.
13. An article comprising:
- a machine-readable medium having stored thereon:
- instructions which, when executed by a data processing device, cause the data processing device to:
- transfer control to a software test module when a second instruction having an instruction address in the instructions is executed by the data processing device, the second instruction replacing a non-identical first instruction comprising a target address;
- a test module, the test module comprising
- a table comprising a target address of the replaced first instruction; and
- test instructions to produce a test result by performing a test on the instructions, the test module to locate the target address in the table and to transfer control to the target address if the test result indicates the instructions are to proceed.
14. The article of claim 13 in which the instructions further comprise
- instructions to load the test module.

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15. The article of claim 13 in which the test module further comprises instructions to set an exception handler to transfer control to the test instructions when the second instruction is executed by the data processing device.

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16. The article of claim 14 in which the test module further comprises:
instructions moved from the instructions, the instructions moved to make room in the instructions for the instructions to load the test module.